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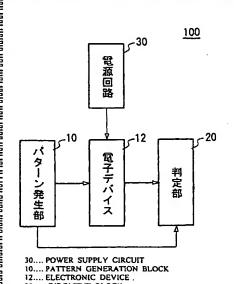
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(81) 指定国 (国内): JP, KR, US.

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(54) Title: POWER SUPPLY CIRCUIT AND TEST APPARATUS

(54) 発明の名称: 電源回路、及び試験装置



20.... JUDGMENT BLOCK

る電源回路を提供する。

(57) Abstract: A power supply circuit for supplying voltage to a load. The circuit includes a power supply block for generating a predetermined voltage, an electric route for connecting the power supply block to the load, a current lead-in block for leading in current from the electric route, and a current control block for controlling the current led in by the current lead-in block from the electric route, according to the voltage received by the load.

(57) 要約:

負荷に電圧を供給する電源回路であって、予め定められた電圧を発生する電源部と、電源部と負荷とを電気的に接続する電気的経路と、電気的経路から電流を引き込む電流引き込み部と、負荷が受け取る電圧に基づいて、電流引き込み部が電気的経路から引き込む電流を制御する電流制御部とを備えることを特徴とす

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INTERNATIONAL SEARCH REPORT

International application No. PCT/JP02/05607

	SIFICATION OF SUBJECT MATTER C1 ⁷ G05F1/10		
According t	o International Patent Classification (IPC) or to both na	ational classification and IPC	
B. FIELD	S SEARCHED		
Minimum d	ocumentation searched (classification system followed	by classification symbols)	
Int.	C1 ⁷ G05F1/10		
	tion searched other than minimum documentation to the		
Koka	uyo Shinan Koho 1926—1996 i Jitsuyo Shinan Koho 1971—2002	Toroku Jitsuyo Shinan Koho Jitsuyo Shinan Toroku Koho	5 1996–2002
Electronic d	lata base consulted during the international search (nam	ne of data base and, where practicable, sear	rch terms used)
C. DOCU	MENTS CONSIDERED TO BE RELEVANT		·
Category*	Citation of document, with indication, where ap		Relevant to claim No.
X Y	EP 0597426 A2 (Matsushita El 18 May, 1994 (18.05.94),	ectric Works, Ltd.),	1-15 16
Y	18 May, 1994 (18.05.94), Fig. 7		10
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I	Fig. 2 (Family: none)		10
v	TD 0 000000 % /EDV 0000 \		1 10 12_15
X Y	JP 2-280668 A (FDK Corp.), 16 November, 1990 (16.11.90),		1-10,12-15 16
-	Fig. 2		
	(Family: none)	}	
		•	
× Furth	er documents are listed in the continuation of Box C.	See patent family annex.	
	categories of cited documents: ent defining the general state of the art which is not	"T" later document published after the inte priority date and not in conflict with th	mational filing date or
conside	red to be of particular relevance	understand the principle or theory under	criying the invention
"E" carlier date	document but published on or after the international filing	"X" document of particular relevance; the considered novel or cannot be considered.	elaimed invention cannot be elected to involve an inventive
"L" docume	ent which may throw doubts on priority claim(s) or which is establish the publication date of another citation or other	step when the document is taken alone "Y" document of particular relevance; the	laimed invention cannot be
	reason (as specified) ent referring to an oral disclosure, use, exhibition or other	considered to involve an inventive step combined with one or more other such	documents, such
"P" docum	ent published prior to the international filing date but later e priority date claimed	combination being obvious to a person document member of the same patent if	
Date of the a	actual completion of the international search	Date of mailing of the international search	ch report
09 S	eptember, 2002 (09.09.02)	24 September, 2002	
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer	
Facsimile No.		Telephone No.	

INTERNATIONAL SEARCH REPORT

International application No.
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ategory*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Υ .	JP 2001-124821 A (Ando Electric Co., Ltd.), 11 May, 2001 (11.05.01), Fig. 1 (Family: none)	
Y	JP 2001-108725 A (Advantest Corp.), 20 April, 2001 (20.04.01), Fig. 6 (Family: none)	16
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